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# The Glitch In Sleep Seems 1 John Hulme

**coding and scripting techniques for fsm designs with ...** - coding and scripting techniques for fsm designs with synthesis-optimized, glitch-free outputs clifford e. cummings sunburst design, inc. abstract **dac8550 16-bit, ultra-low glitch, voltage output digital ...** - dac8550 16-bit, ultra-low glitch, voltage output digital ... .. dac ... **dac7568, dac8168, dac8568 12-/14-/16-bit, octal-channel ...** - dac7568 dac8168 dac8568 data buffer a dac register a buffer control register control control logic 32-bit shift register power-down control logic av dd vref refin/v out **ni 6321 specifications - national instruments** - device specifications ni 6321 x series data acquisition: 250 ks/s, 16 ai, 24 dio, 2 ao the following specifications are typical at 25 °c, unless otherwise noted. **ni 6351 device specifications - national instruments** - device specifications ni 6351 x series data acquisition: 1.25 ms/s, 16 ai, 24 dio, 2 ao the following specifications are typical at 25 °c, unless otherwise noted. **chi-square: testing for goodness of fit** - 4{2 chi-square: testing for goodness of t the  $\chi^2$  distribution the quantity  $\sim 2$  de ned in eq. 1 has the probability distribution given by  $f(\sim 2) = 1$  **12-bit dac with eeprom memory in sot-23-6** - pin board. 2 sot-23

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